

# TSA50R240S1

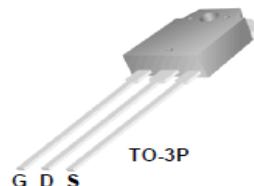
## 500V 18A N-Channel SJ-MOSFET

### General Description

Truesemi SJ-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.

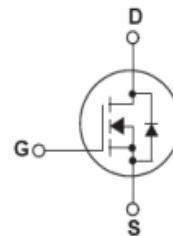
This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy.

SJ-FET is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.



### Features

- 550V @T<sub>J</sub> = 150 °C
- Typ. R<sub>D(on)</sub> = 0.21Ω
- Ultra Low gate charge (typ. Q<sub>g</sub> = 43nC)
- 100% avalanche tested



### Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
V <sub>DSS</sub>	Drain-Source Voltage	500	V
I <sub>D</sub>	Drain Current -Continuous (TC = 25°C) -Continuous (TC = 100°C)	18* 11*	A
I <sub>DM</sub>	Drain Current – Pulsed (Note 1)	55*	A
V <sub>GSS</sub>	Gate-Source voltage	±30	V
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)	284	mJ
I <sub>AR</sub>	Avalanche Current (Note 1)	2.4	A
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)	0.43	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	15	V/ns
P <sub>D</sub>	Power Dissipation (TC = 25°C)	104	W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to +150	°C
T <sub>L</sub>	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300	°C

\* Drain current limited by maximum junction temperature.

### Thermal Characteristics

Symbol	Parameter	Value	Unit
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case	1.2	°C/W
R <sub>θCS</sub>	Thermal Resistance, Case-to-Sink Typ.	0.5	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	62	°C/W

## Electrical Characteristics TC = 25°C unless otherwise noted

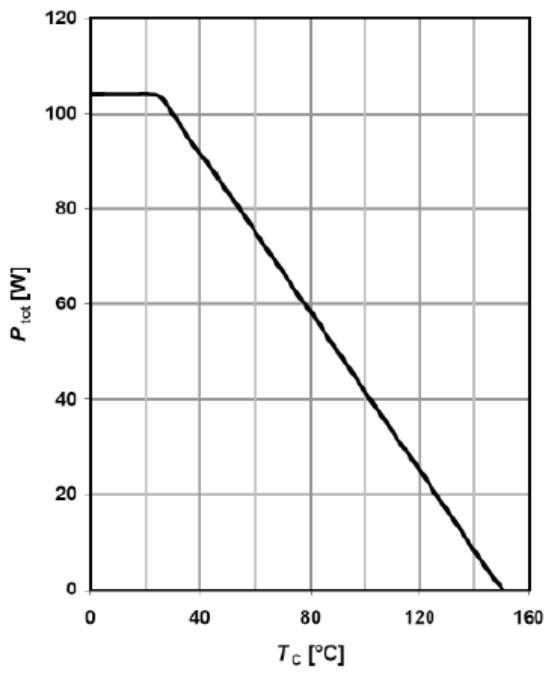
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Off Characteristics						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A, T_J = 25^\circ C$	500	--	--	V
		$V_{GS} = 0V, I_D = 250\mu A, T_J = 150^\circ C$	--	550	--	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250\mu A$ , Referenced to $25^\circ C$	--	0.6	--	V/°C
$I_{DS(on)}$	Zero Gate Voltage Drain Current	$V_{DS} = 500V, V_{GS} = 0V, T_J = 150^\circ C$	--	-- 10	1	$\mu A$
$I_{GSSF}$	Gate-Body Leakage Current, Forward	$V_{GS} = 30V, V_{DS} = 0V$	--	--	100	nA
$I_{GSSR}$	Gate-Body Leakage Current, Reverse	$V_{GS} = -30V, V_{DS} = 0V$	--	--	-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.5	--	4.5	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10V, I_D = 9A$	--	0.21	0.24	$\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS} = 40V, I_D = 18A$	--	16	--	S
$R_g$	Gate resistance	f = 1MHz ,open drain	--	3.5	--	$\Omega$
Dynamic Characteristics						
$C_{iss}$	Input Capacitance	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$	--	800	--	pF
$C_{oss}$	Output Capacitance		--	340	--	pF
$C_{rss}$	Reverse Transfer Capacitance		--	10	--	pF
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 400V, I_D = 9A$ $R_G = 20\Omega$ (Note 4)	--	13	--	ns
$t_r$	Turn-On Rise Time		--	11	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	100	--	ns
$t_f$	Turn-Off Fall Time		--	12	--	ns
$Q_g$	Total Gate Charge	$V_{DS} = 480V, I_D = 9A$ $V_{GS} = 10V$ (Note 4)	--	43	--	nC
$Q_{gs}$	Gate-Source Charge		--	5	--	nC
$Q_{gd}$	Gate-Drain Charge		--	22	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
$I_s$	Maximum Continuous Drain-Source Diode Forward Current	--	--	18	A	
$I_{SM}$	Maximum Pulsed Drain-Source Diode Forward Current	--	--	55	A	
$V_{SD}$	Drain-Source Diode Forward Voltage	$V_{GS} = 0V, I_F = 9A$	--	0.9	1.5	V
$t_{rr}$	Reverse Recovery Time	$V_{GS} = 0V, I_F = 9A$ $dI_F/dt = 100A/\mu s$	--	345	--	ns
$Q_{rr}$	Reverse Recovery Charge		--	4.5	--	$\mu C$

### NOTES:

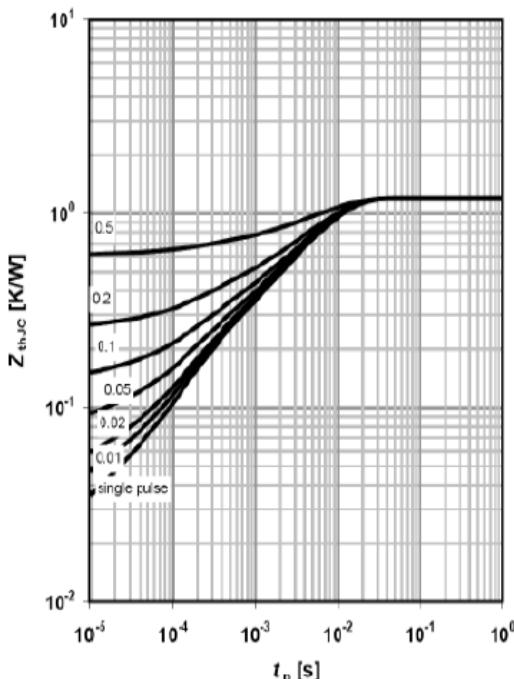
1. Repetitive Rating: Pulse width limited by maximum junction temperature
2.  $I_{AS}=2.4A$ ,  $V_{DD}=50V$ , Starting  $TJ=25^\circ C$
3.  $I_{SD}\leq 18A$ ,  $dI/dt \leq 200A/\mu s$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $TJ = 25^\circ C$
4. Essentially Independent of Operating Temperature Typical Characteristics

# Typical Performance Characteristics

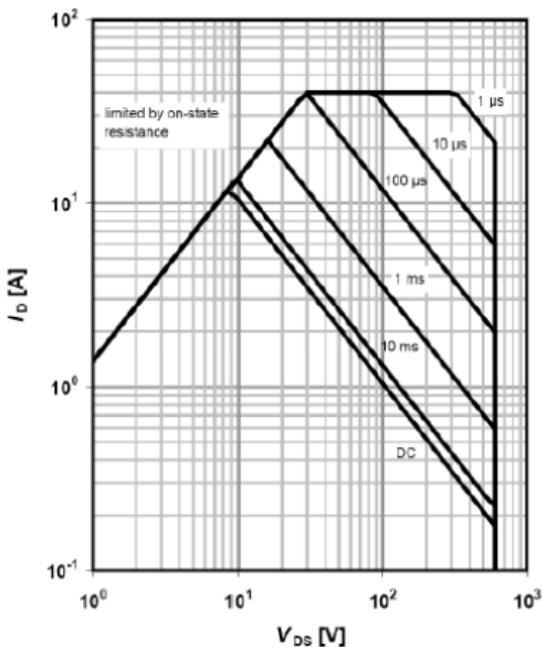
Power dissipation



Max. transient thermal impedance

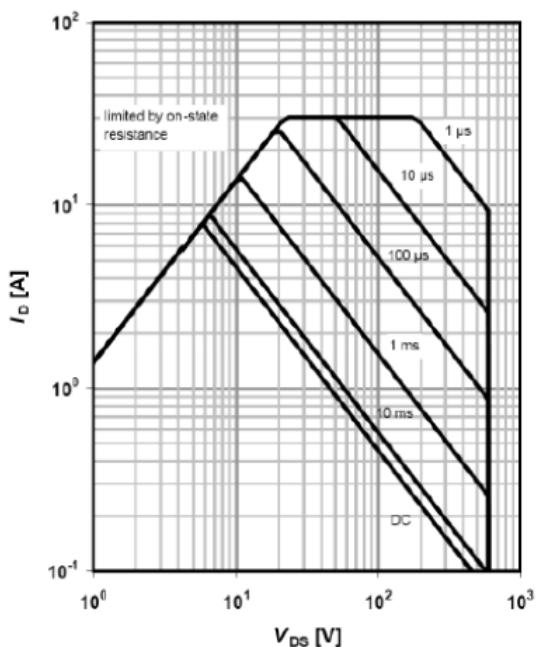


Safe operating area  $T_C=25^\circ C$



$I_D=f(V_{DS})$ ;  $T_C=25^\circ C$ ;  $V_{GS} > 7V$ ;  
 $D=0$ ; parameter  $t_p$

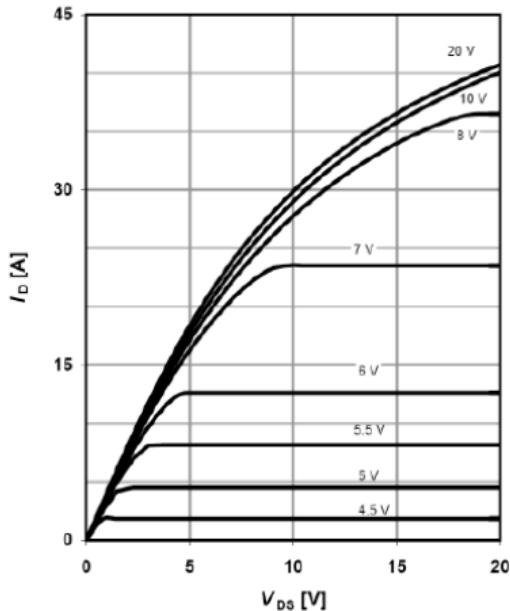
Safe operating area  $T_C=80^\circ C$



$I_D=f(V_{DS})$ ;  $T_C=80^\circ C$ ;  $V_{GS} > 7V$ ;  
 $D=0$ ; parameter  $t_p$

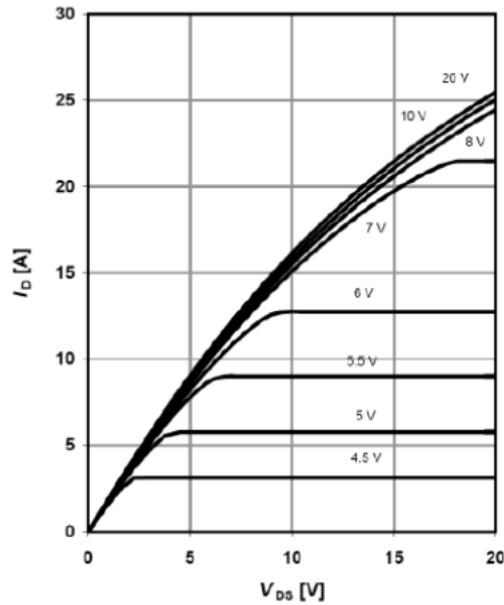
# Typical Performance Characteristics

Typ. output characteristic



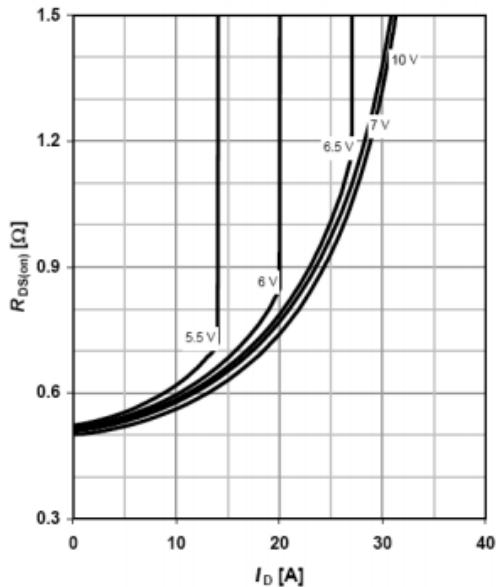
$I_D=f(V_{DS})$ ;  $T_j=25\text{ }^\circ\text{C}$ ;  
parameter  $t_p=10\mu\text{s}$ ,  $V_{GS}$

Typ. output characteristic



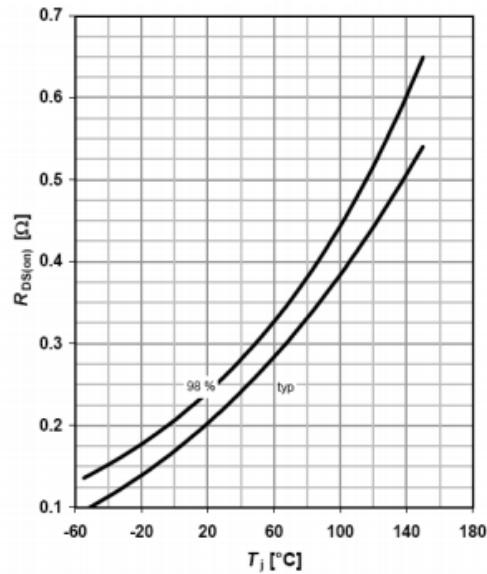
$I_D=f(V_{DS})$ ;  $T_j=125\text{ }^\circ\text{C}$ ;  
parameter  $t_p=10\mu\text{s}$ ,  $V_{GS}$

Typ. Drain-Source on resistance



$R_{Dson}=f(I_D)$ ;  $T_j=125\text{ }^\circ\text{C}$ ; parameter  $V_{GS}$

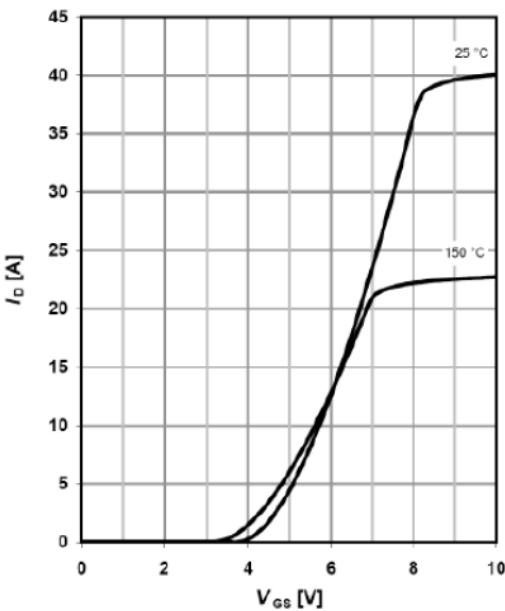
Typ. Drain-Source on resistance



$R_{Dson}=f(T_j)$ ;  $T_j=125\text{ }^\circ\text{C}$ ; parameter  
 $I_D=6.5\text{A}$ ,  $V_{GS}=10\text{V}$

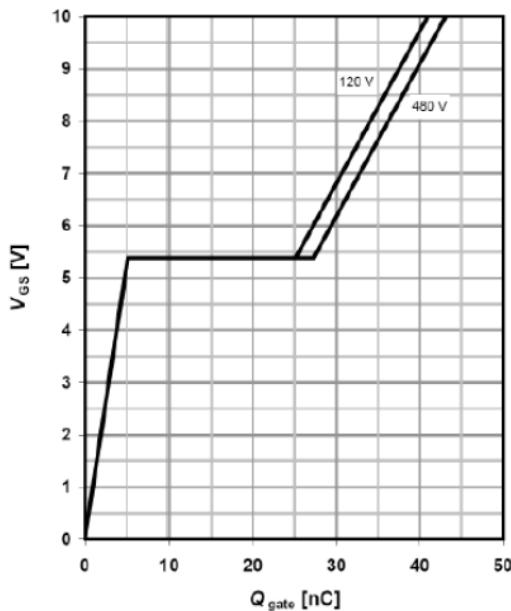
# Typical Performance Characteristics

Typ. Transfer characteristic



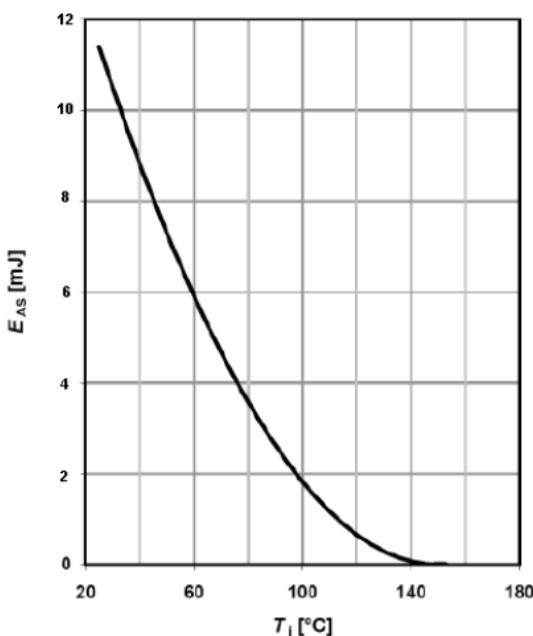
$I_D=f(V_{DS})$ ;  $V_{DS}=20\text{V}$ ;  
parameter  $t_p=10\mu\text{s}$ ,

Typ. gate charge



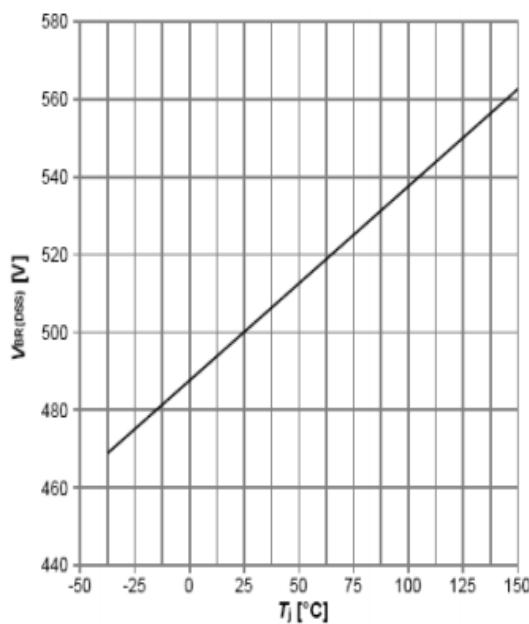
$V_{GS}=f(Q_g)$ ,  $I_D=6.5\text{ A}$  pulsed

Avalanche energy



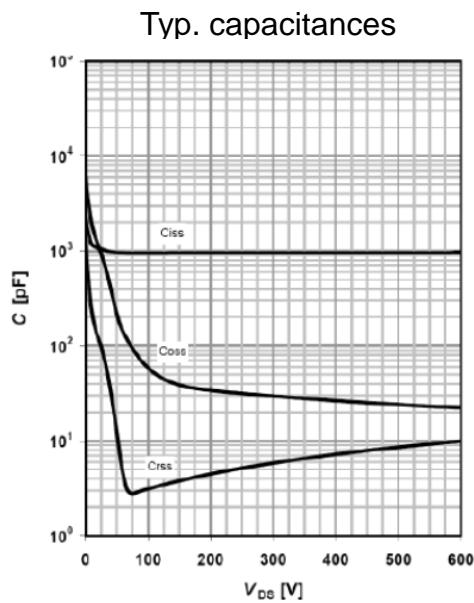
$E_{AS}=f(T_j)$ ;  $I_D=2.4\text{ A}$ ;  $V_{DD}=50\text{ V}$

Drain-source breakdown voltage

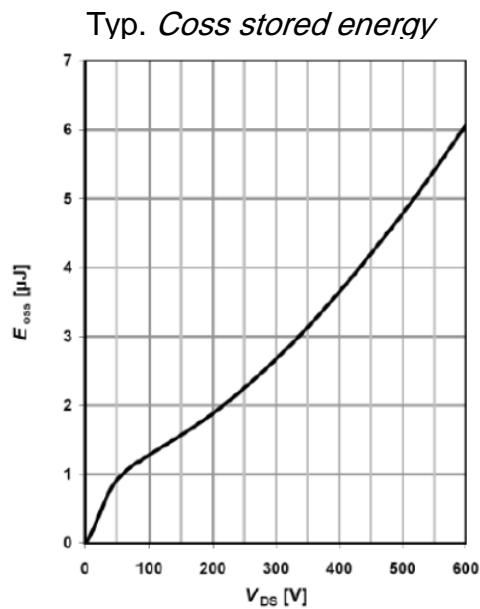


$V_{BR(DSS)}=f(T_j)$ ;  $I_D=1\text{ mA}$

# Typical Performance Characteristics

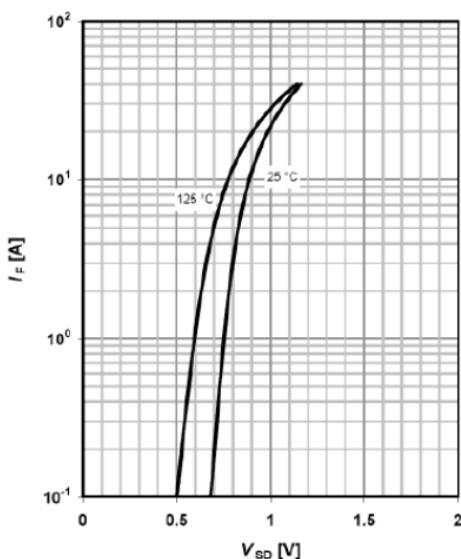


$C = f(V_{DS})$ ;  $V_{GS} = 0$  V;  $f = 1$  MHz



$E_{OSS} = f(V_{DS})$

## Forward characteristics of reverse diode

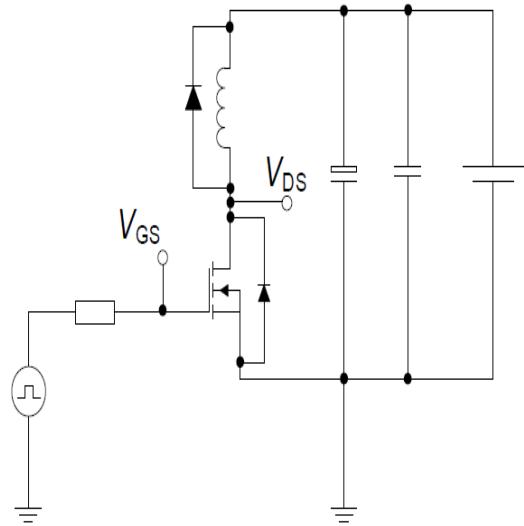


$I_F = f(V_{SD})$ ; parameter: T<sub>j</sub>

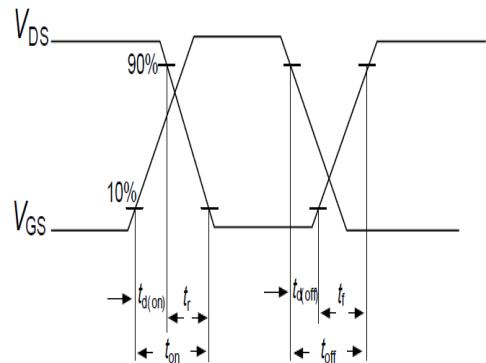
# Test circuits

## Switching times test circuit and waveform for inductive load

Switching times test circuit for inductive load

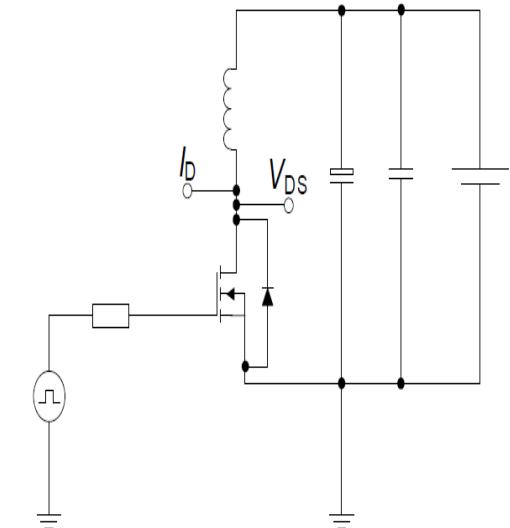


Switching time waveform

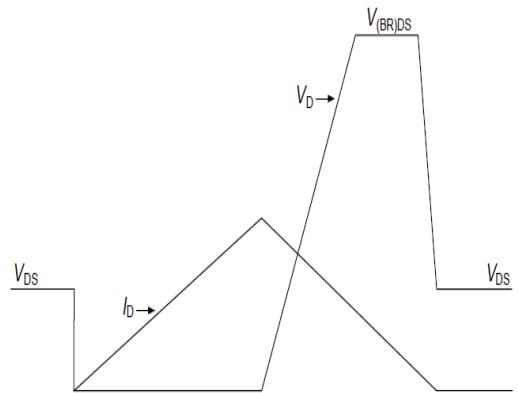


## Unclamped inductive load test circuit and waveform

Unclamped inductive load test circuit



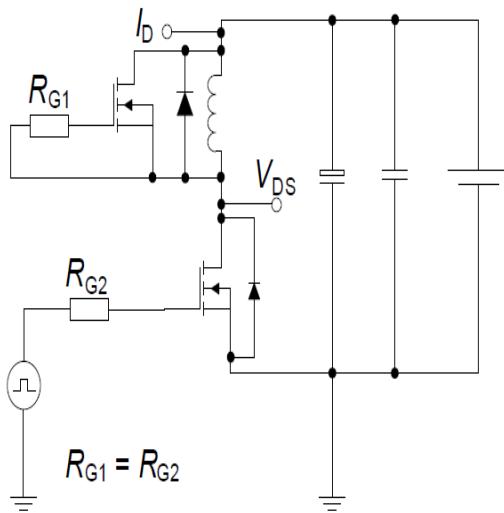
Unclamped inductive waveform



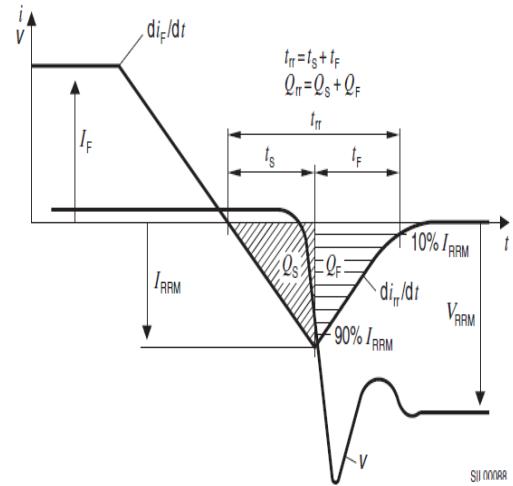
## Test circuits

## **Test circuit and waveform for diode characteristics**

## Test circuit for diode characteristics

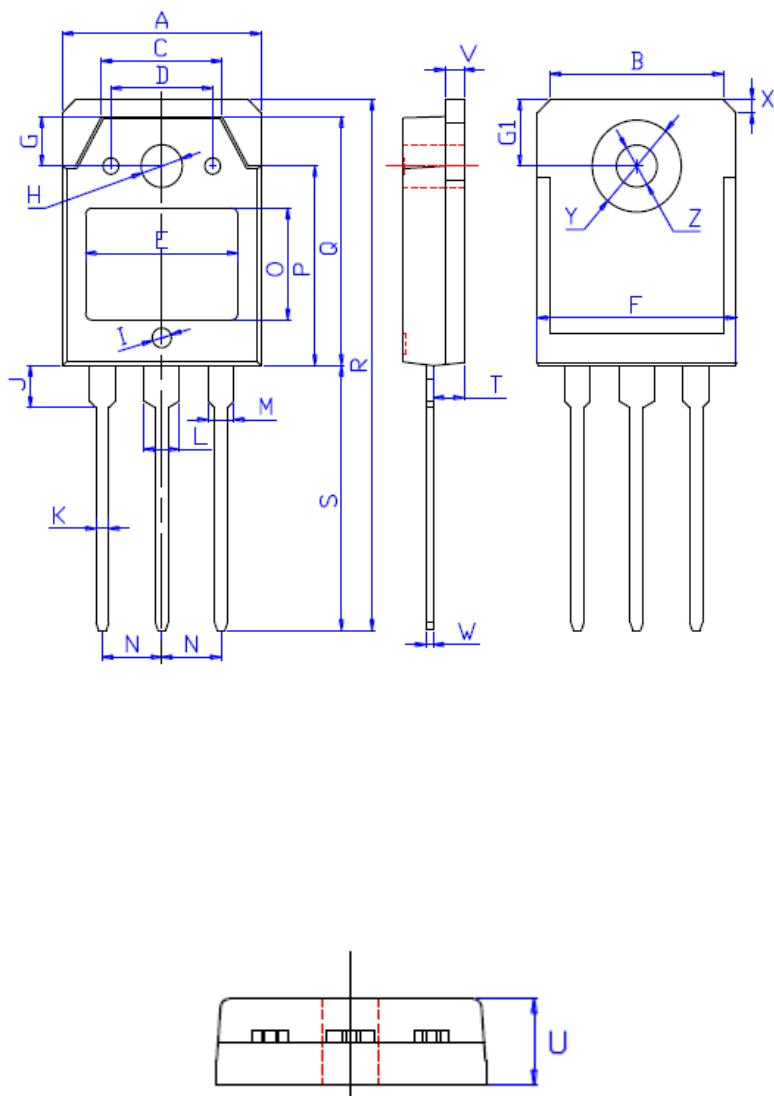


## Diode recovery waveform



# Package Outline TO-3P

TSA50R240S1 500V 18A N-Channel SJ-MOSFET



DIM	MILLIMETERS
A	15.60±0.30
B	13.60±0.30
C	9.50±0.30
D	8.00±0.30
E	11.85±0.30
F	15.65±0.30
G	3.80±0.30
G1	5.00±0.30
H	Φ 3.50±0.30
I	Φ 1.50±0.30 深 0.15±0.15
J	3.20±0.30
K	1.00±0.15
L	3.10±0.15
M	2.10±0.15
N	5.45±0.30
O	8.40±0.30
P	13.90±0.30
Q	18.70±0.30
R	40.00±0.60
S	20.00±0.40
T	2.40±0.30
U	4.80±0.30
V	1.50±0.15
W	0.60±0.15
X	1.80±0.40
Y	7.00±0.30
Z	3.20±0.30